|  |  |  |
| --- | --- | --- |
|  |

|  |
| --- |
| Name: **Dr. Dilip Kumar Nayak** Designation: **Professor**E-Mail: **nayakdilipk@gmail.com**Area of Specialization: **Signal Processing**Affiliation: **Department of Electronics & Communication Engineering, Einstein Academy of Technology & Management, Khordha, Bhubaneswar, Odisha**  |

 |

|  |
| --- |
| **Personal Details:**  |
| Date of Birth: 23-07-1974 Marital Status: Married Permanent Address: Flat No. 301, Block 5, Supratik Elegance Jagamara, Khandagiri, Bhubaneswar-751030 E-Mail: nayakdilipk@gmail.comContact Nos.: 9178043725Skype ID: Scopus ID:**16069186500** ISTE Membership No: **LM-134853**IETE:**F232552**IEEE:**91167109**ORCID:**0000-0002-8231-0277**

|  |
| --- |
| **Education:**  |
| ***Degree***  | ***Specialization*** | ***University / Institute***  |
| Ph.D  | Fault Monitoring and Diagnosis of Mineral Processing Equipment using Signal Processing Techniques | Siksha ‘O’ Anusandhan, Bhubaneswar, India |
| M.Tech  | Computer Applications in Industrial Drives | Visveswariah Technological University, Karnataka |
| A.M.I.E | Electrical Engineering | Institution of Engineers (India) |

 |

|  |
| --- |
|  **Academic Experience:**  |
| 1. **Professor**, Einstein Academy of Technology and Management, Bhubaneswar, from

01-10-2021 to till date.1. **Professor**, Gandhi Institute for Education & Technology, Baniatangi, Bhubaneswar,

from 21-12-2020 to 30-09-2021.1. **Assistant Professor**, C.V Raman College of Engineering, Bhubaneswar, from 12-

06-2002 to 26-04-2018.

|  |
| --- |
| **Subjects Handled:**  |
| **U.G.**  | **P.G.**  |
| * Signals & Systems.
* Digital Signal Processing.
* Microprocessors &Microcontrollers.
* Basic Electronics.
* Analog Electronics.
* Digital Electronics.
* VLSI Design.
 | * Digital IC Design
 |
| **Research Interests:**  |
| Vibration Signal Processing, Transform Domain Signal Processing, VLSI Design. |
| **International Journals:**  |
| 1. **D. K. Nayak,** D. P. Das, S. K. Behera, S. P. Das, and J. K. Sadangi, “Prevention of Hydrocyclone Choking through Detection of Sub-Hz Frequency Shift of Vibration Signal,” Journal of Vibration Engineering & Technologies (Springer), Volume.8, No. 4, pp.517-528, 2020, DOI: https://doi.org/10.1007/s42417-019-00107-2.
2. **D. K. Nayak,** D. P. Das, S. K. Behera, and S. P. Das, “Monitoring the Fill Level of a Ball Mill using Vibration Sensing and Artificial Neural Network,” Neural Computing and Applications (Springer), Volume. 32, No. 5, pp.1501-1511, 2020, DOI: <https://doi.org/10.1007/s00521-019-04555-5>.

|  |
| --- |
| **Conference Papers:**  |
| 1. **D. K. Nayak,** D. P. Das, S. K. Behera, and S. P. Das, “Wavelet-based Classification of Mineral Hardness by Vibration Signal Processing of a Ball Mill,” in Proc. International Conference on Recent Trends in Computational Engineering and Technologies(**ICRTCET-2018**), Bangalore, pp.744-749, 2018.
2. **D. K. Nayak,** D. P. Das, S. K Behera, S. P. Das, and J. K. Sadangi, “Detection of Hydrocyclone Choking using Vibration Signal Processing,” in Proc.3rd International Conference on Recent Trends in Computer Science and Technology (**ICRTCST-2018**), Jamshedpur, Published in International Journal of Computer Engineering and Applications, Volume-XII, pp.1-6, 2018.
3. D. P. Das, G. Panda, and **D. K. Nayak**, “Development of Frequency Domain Block Filtered-S LMS (FBFSLMS) Algorithm for Active Noise Control System,” in Proc. **IEEE** International Conference on Acoustics, Speech and Signal Processing**(ICASSP-2006**), France, Volume V, pp.289-292, 2006, **DOI:** https://doi.org/10.1109/ ICASSP.2006.1661269.

**Conferences / STTPs / FDPs / Seminars Organized:** 1. “**Signal Processing for Biomedical Applications**” at C.V Raman College of Engineering, Bhubaneswar from 05-12-2016 to 10-12-2016(06 Days).
2. **“Where is Intelligent Computing?”** at C.V Raman College of Engineering, Bhubaneswar on 09-09-2011(01 Day).
 |

**STTPs / FDPs / Seminars / Workshops Participated:**1. Cognitive Learning Instructor (Foundation Level Training) organised by **BPUT, Odisha and Illumine Foundation** from 11-01-2021 to 19-01-2021(09 Days).
2. Machine Learning for Data Science using Python, Workshop organised by **SIT, Bhubaneswar** from 15-01-2020 to 19-01-2020 (05 Days).
3. Brain Science & Technology: Tools and Techniques to understand the Human Brain, Short Term Training Program organised by **IIT Delhi** from 14.12.2016 to 18.12.2016 (05 Days).
4. 2G vs. 3G, Short Term Course organised by **NITTTR, Chandigarh** from 23.05.2016 to 27.05.2016 (05 Days).
5. Design of Experiments: An Optimisation Tool organised by **NIT Rourkela** from 21.12.2015 to 24.12.2015(04 Days).
6. Use of ICT in Teaching- Learning Process, organised by **NITTTR, Kolkata** from 01.06.2015 to 05.06.2015(05 Days).
7. LabVIEW DAQ, organised by **C.V Raman College of Engineering, Bhubaneswar** from 14.11.2014 to 15.11.2014(02 Days).
8. Advances in Computational Intelligence, Summer School jointly organised by **IEEE Computational Intelligence Society (Calcutta Chapter) & SOA University, Bhubaneswar** from 14-04-2014 to 15-04-2014(02 Days).
9. Advances in Signal Processing for Power, Control & Communication, National Workshop organised by **IIIT Bhubaneswar** from 27.06.2013 to 29.06.2013(03 Days).
10. Computational Intelligence: Theories, Applications and Future Directions, National Workshop jointly organised by **IEEE Computational Intelligence Society & SOA University, Bhubaneswar** on 12-07-2013(01 Day).
11. MATLAB® & Simulink® for Engineering Education, A Seminar organised by **MathWorks** India on 21-09-2012(01 Day).
12. Recent Trends in VLSI Design and Embedded Systems, A Faculty Development Program organised by **BPUT, Odisha** from 26.07.2012 to 28.07.2012(03 Days).
13. Advanced DSP Design Techniques, A Short Term Course organised by **IIT, Kharagpur** from 02-07-2012 to 06-07-2012(05 Days).

**Achievements and Awards:** * **Gold Medal in M.Tech**

**Professional Society Memberships:** 1. **Fellow- IETE(India)**
2. **Senior Member-IEEE(USA)**
3. **Member- IEEE Signal Processing Society (USA)**
4. **Life Member- ISTE(India)**
 |

 |